

In re Patent Application of:
JOFFE ET AL.
Serial No. **09/997,228**
Filing Date: **11/29/01**

REMARKS

Claims 1 and 4-16 remain in this application.
Claims 2, 3 and 17-20 are cancelled. Claims 1, 4, 6, 9, 10,
and 11 are amended.

Applicants thank the Examiner for the detailed study of the application and prior art and the suggestions for correcting inconsistencies in the claims. Applicants note the continued rejection of claims as anticipated by U.S. Patent No. 5,585,763 to Navabi et al. (hereinafter "Navabi"). Applicants have again reviewed the application, claims as amended in the previously filed Amendment, Navabi and other prior art, and have amended the claims to place the case in condition for allowance.

The two independent claims 1 and 11 now recite that the current-dependent current source is a current mirror circuit, and as noted, a second operational amplifier as a feedback amplifier is coupled by one input to the current mirror node and another input coupled to the output current node and has an output operatively connected to the current mirror circuit to aid in removing current mirror distortion for values of load resistance.

Navabi clearly discloses a differential gain block 310, but not an operational amplifier and second operational amplifier as a feedback amplifier coupled to the current mirror node and output current node and having an output operatively connected to the current mirror circuit. At most, any suggestion in Navabi for two amplifiers are 300a and 300b in FIG. 6 and they are disclosed as two controlled impedance amplifiers with complimentary signals and a positive output 701 while amplifier 700b has a negative output 702 with each

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output connected to the load 705. Nowhere does it suggest the structure of the first operational amplifier having input coupled to the input port and a single output and a second operational amplifier as a feedback amplifier having inputs coupled to a current mirror node and output current node and having an output operatively connected to the current mirror circuit. This structure in combination with the other elements as claimed helps remove current mirror distortion for values of load resistance. No new matter has been added and the use of the second operational amplifier as a feedback amplifier is shown in FIG. 8 and described relative to FIG. 8 on pages 17 and 18.

Applicants contend that the present case is in condition for allowance and respectfully requests that the Examiner issue a Notice of Allowance and Issue Fee Due.

If the Examiner has any questions or suggestions for placing this case in condition for allowance, the undersigned attorney would appreciate a telephone call.

Respectfully submitted,

A large, stylized handwritten signature in black ink, appearing to read "Richard K. Warther".

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